

Fig. 1

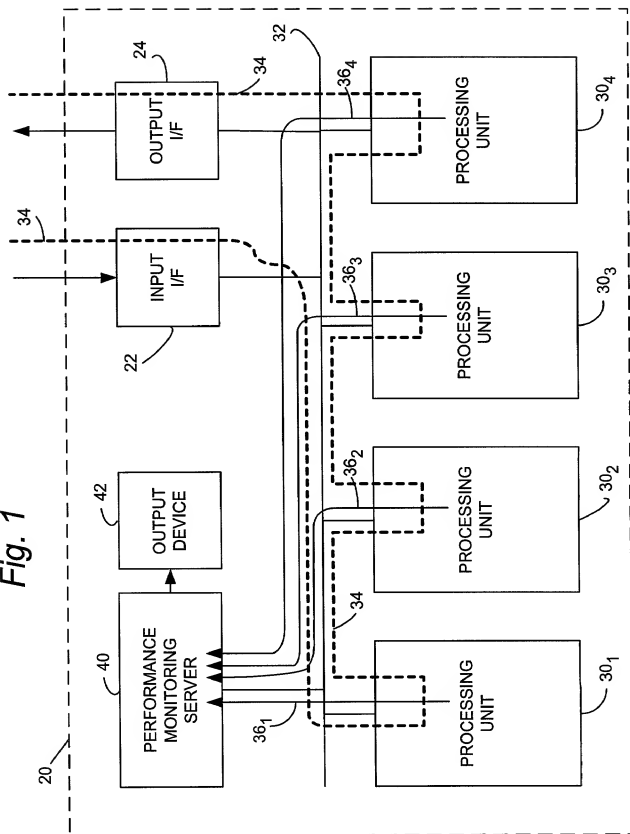


Fig. 2

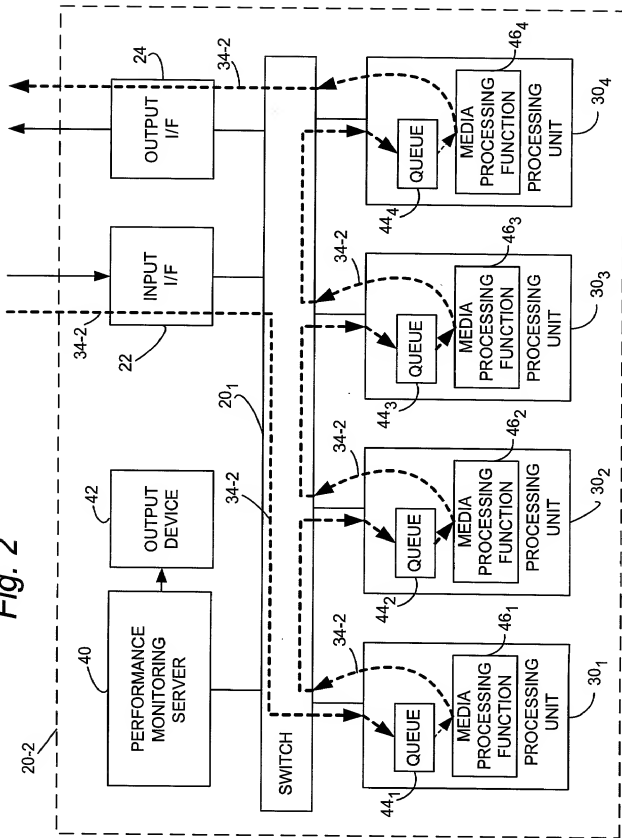


Fig. 3

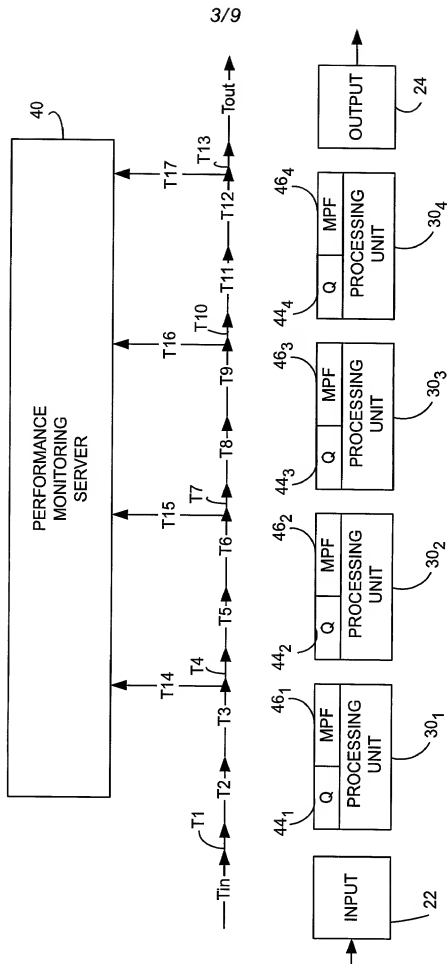
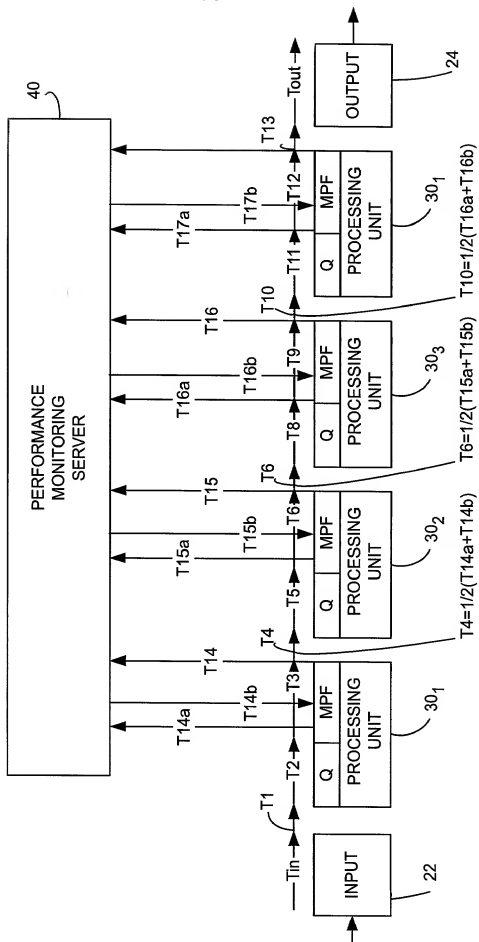
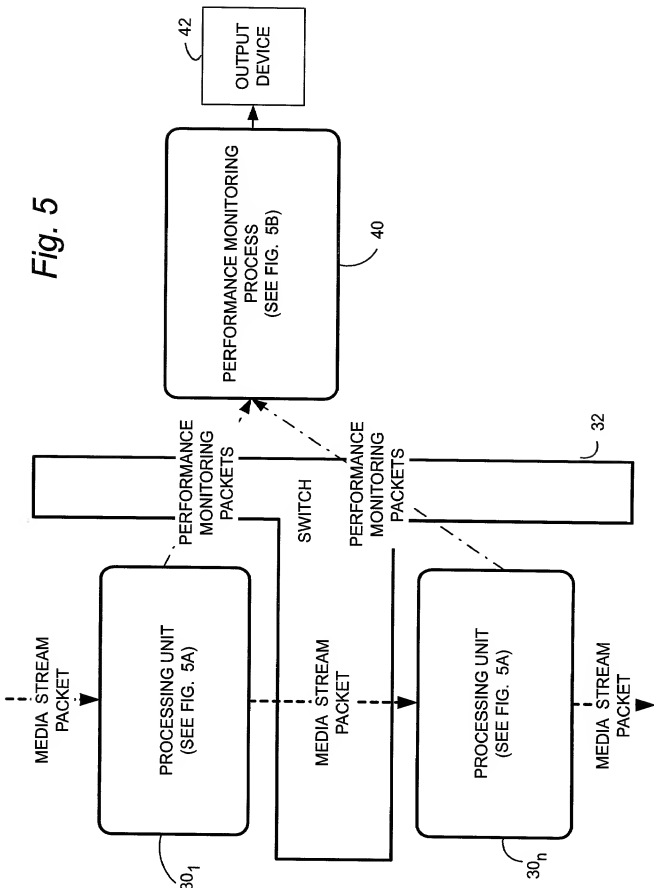


Fig. 4





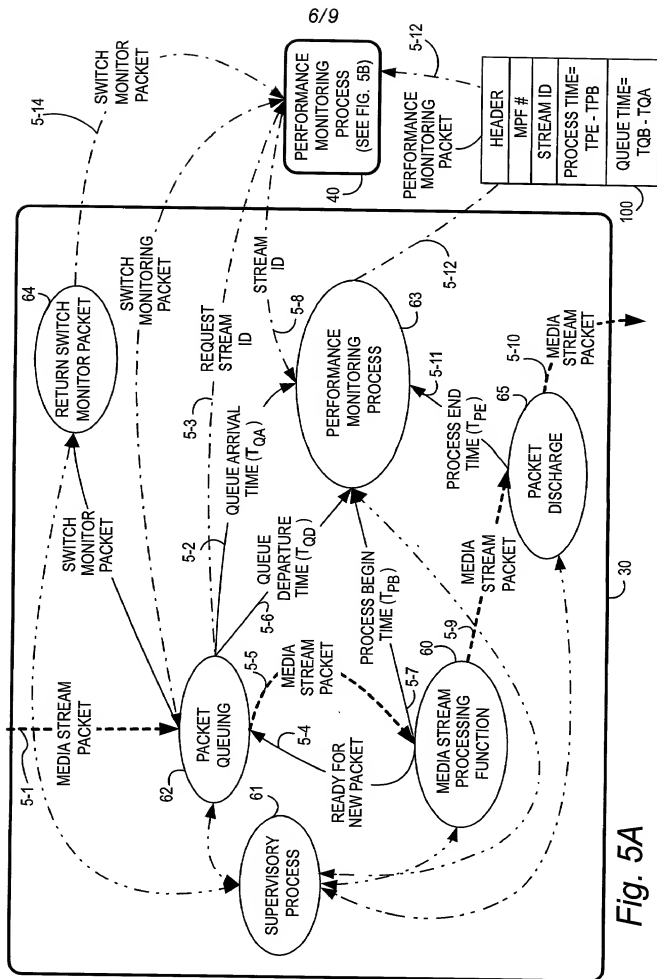


Fig. 5A

Fig. 5B

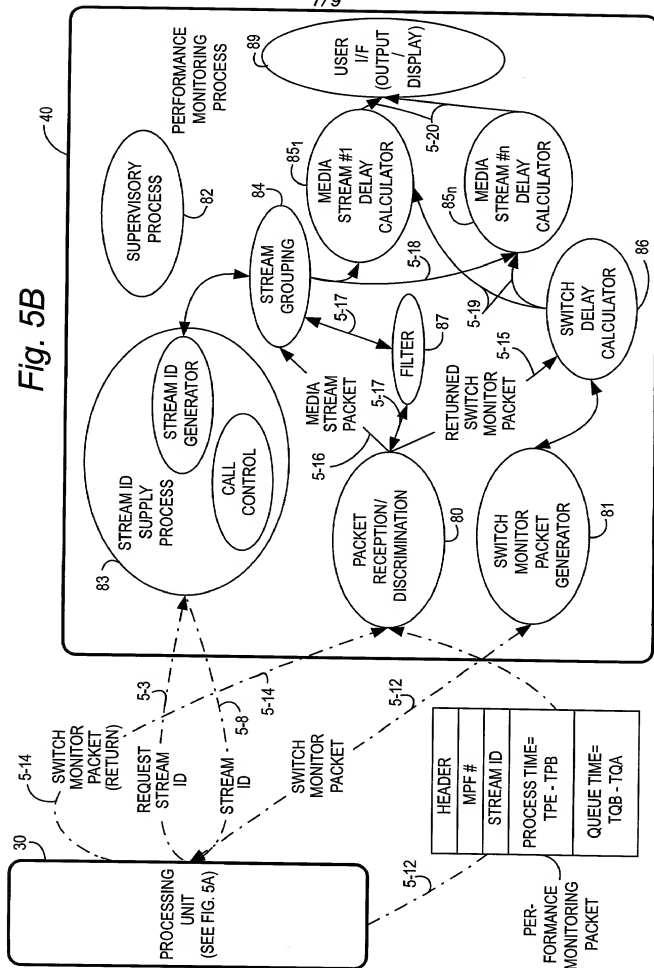


Fig. 6

MEDIA STREAM # 1			
FIRST PROCESSOR (30 <sub>1</sub> )	QUEUE DELAY	QD <sub>1-1</sub>	
	PROCESSING DELAY	PD <sub>1-1</sub>	
	TOTAL DELAY	TD <sub>1-1</sub>	
SECOND PROCESSOR (30 <sub>2</sub> )	QUEUE DELAY	QD <sub>1-2</sub>	
	PROCESSING DELAY	PD <sub>1-2</sub>	
	TOTAL DELAY	TD <sub>1-2</sub>	

...

FOURTH PROCESSOR (30 <sub>4</sub> )	QUEUE DELAY	QD <sub>1-4</sub>	
	PROCESSING DELAY	PD <sub>1-4</sub>	
	TOTAL DELAY	TD <sub>1-4</sub>	
OVERALL NODE DELAY		OND <sub>1</sub>	

MEDIA STREAM # q			
FIRST PROCESSOR (30 <sub>1</sub> )	QUEUE DELAY	QD <sub>q-1</sub>	
	PROCESSING DELAY	PD <sub>q-1</sub>	
	TOTAL DELAY	TD <sub>q-1</sub>	
SECOND PROCESSOR (30 <sub>2</sub> )	QUEUE DELAY	QD <sub>q-2</sub>	
	PROCESSING DELAY	PD <sub>q-2</sub>	
	TOTAL DELAY	TD <sub>q-2</sub>	

OVERALL NODE DELAY	OND <sub>2</sub>
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Fig. 7

